



Response under 37 C.F.R. 1.116
Expedited Procedure
Examining Group 2817

In re Application of

Robert B. Staszewski, et al.

Serial No.: **09/695,516**

Filed: **10/24/2000**

For: **HYBRID OF PREDICTIVE AND CLOSED-LOOP PHASE DOMAIN
DIGITAL PLL ARCHITECTURE**

Docket No.: **TI-30674**

Examiner: **Kinhead**

Art Unit: **2817**

Confirm. No.: **3115**

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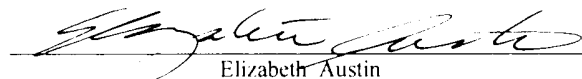
AMENDMENT - 37 CFR § 1.116

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER § 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450 on October 1, 2003.


Elizabeth Austin

Responsive to the Office Action dated August 1, 2003, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.